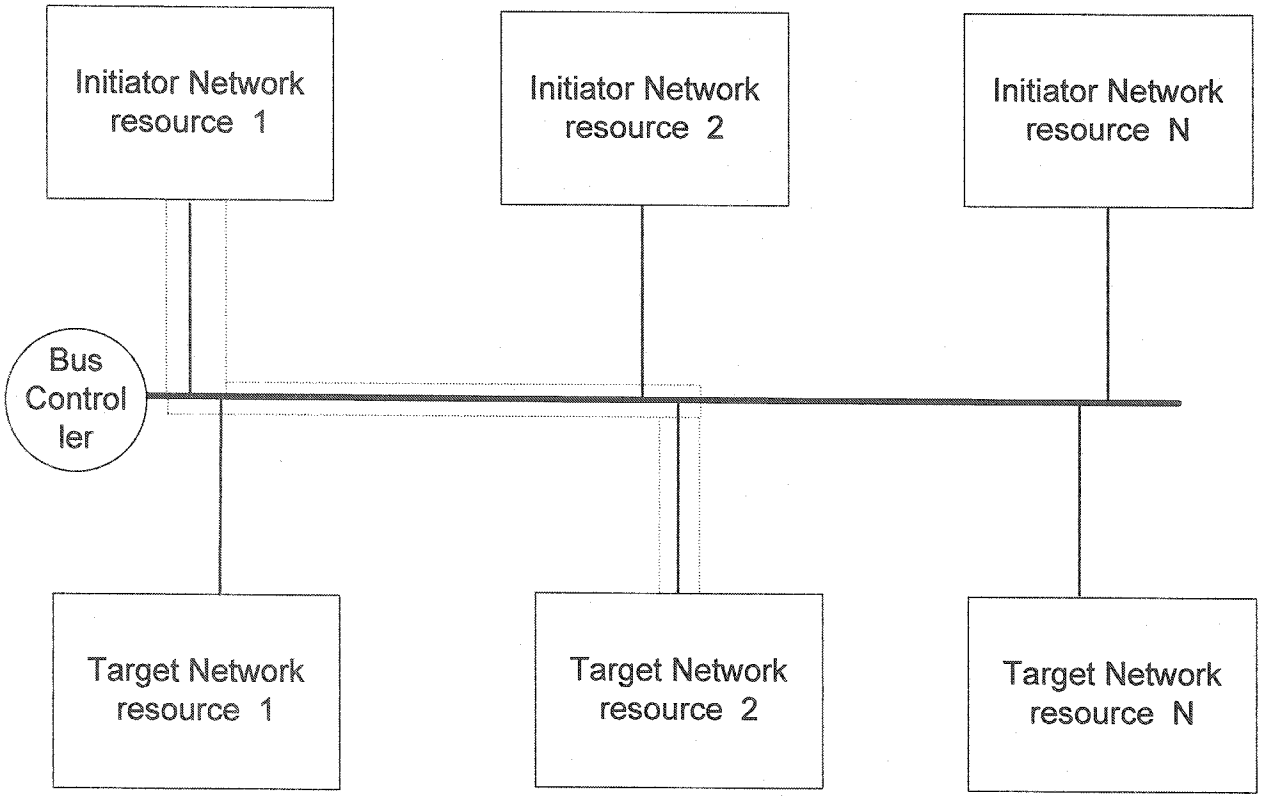

Figure 1

PRIOR ART

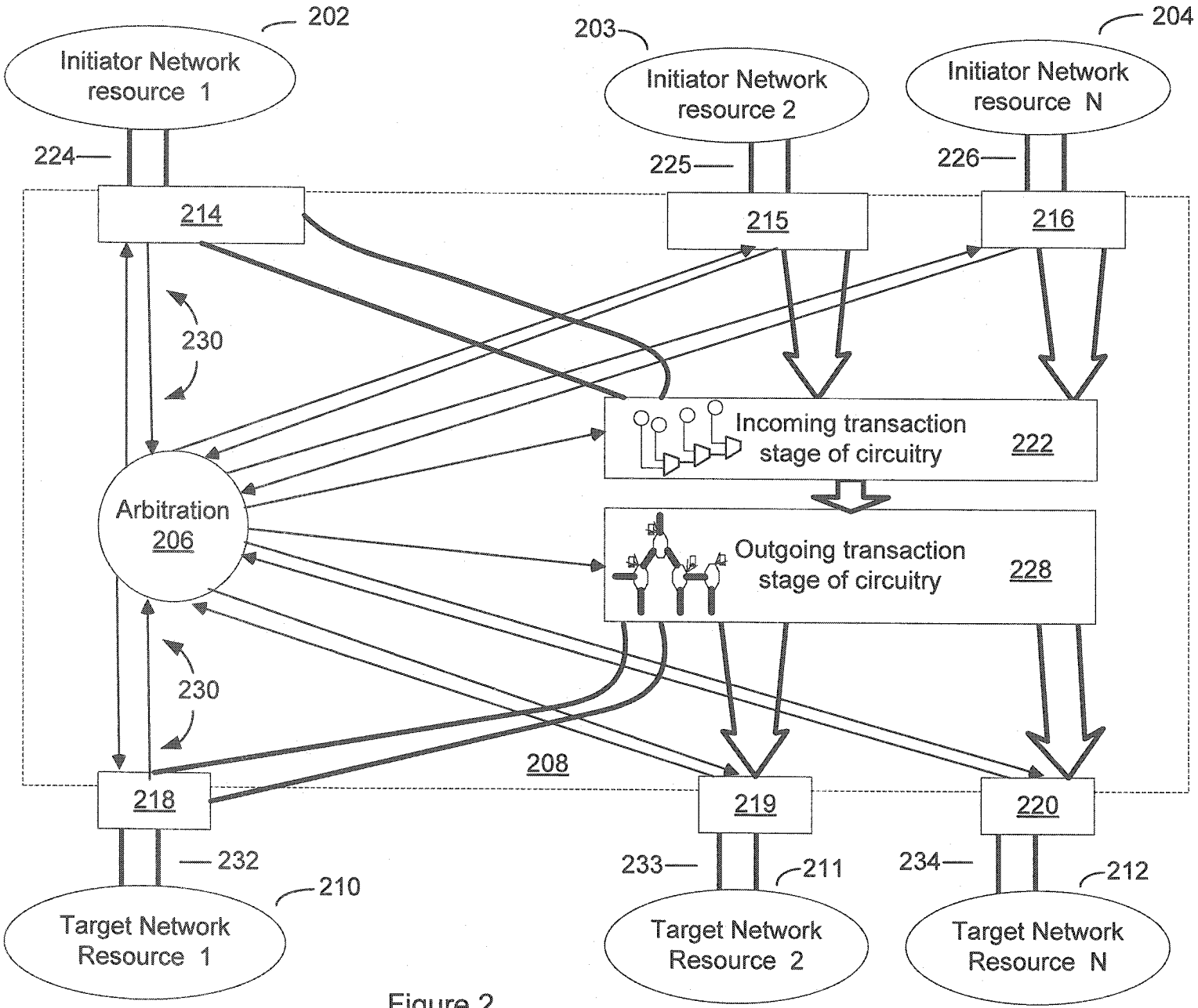
202
Initiator Network resource 1
203
Initiator Network resource 2
204
Initiator Network resource N
224
225
226
214
215
216
230
Incoming transaction stage of circuitry
222
Arbitration 206
Outgoing transaction stage of circuitry
228
230
208
218
219
220
232
233
211
234
212
210
Target Network Resource 1
Target Network Resource 2
Target Network Resource N

Figure 2

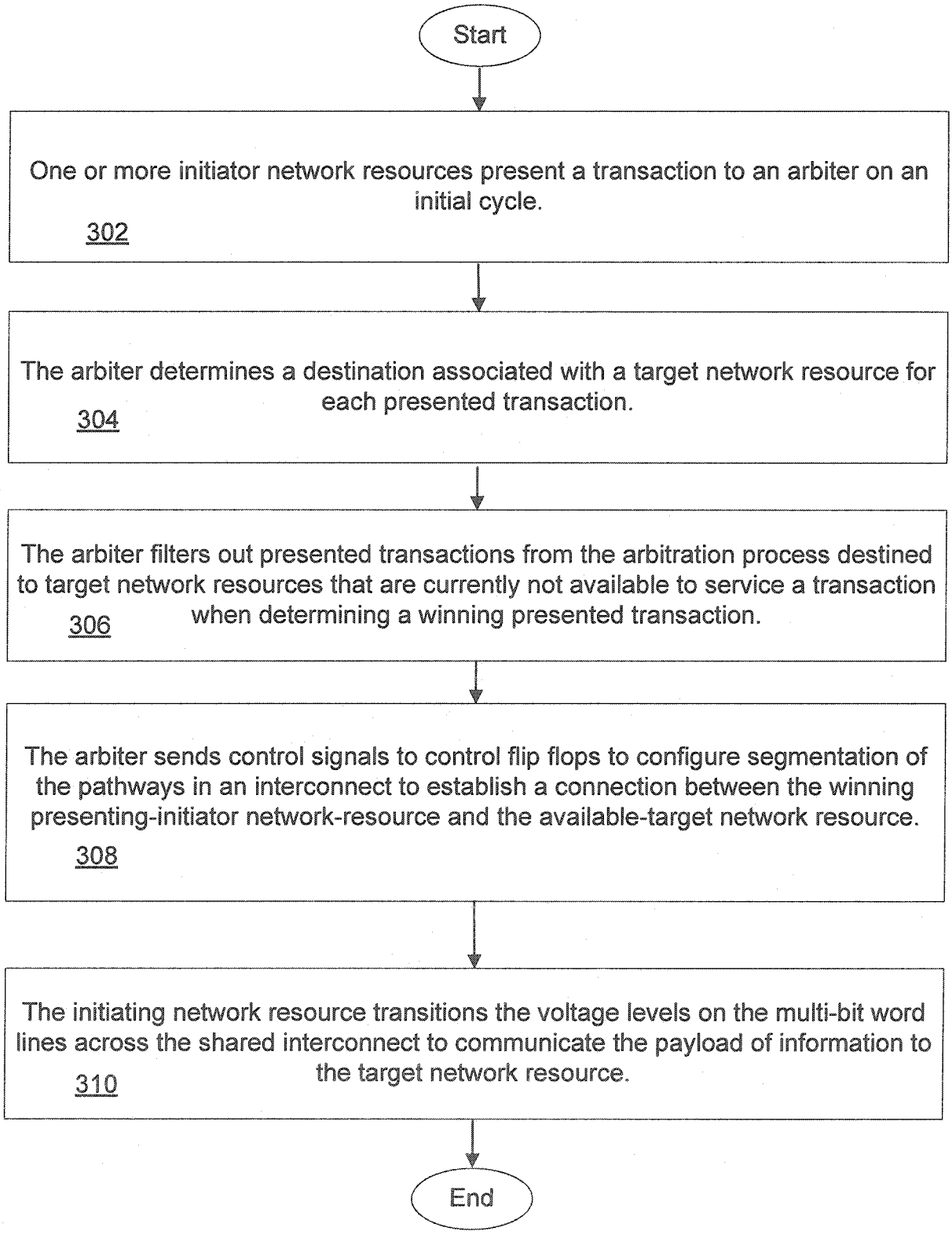
Start
One or more initiator network resources present a transaction to an arbiter on an initial cycle.
302
The arbiter determines a destination associated with a target network resource for each presented transaction.
304
The arbiter filters out presented transactions from the arbitration process destined to target network resources that are currently not available to service a transaction when determining a winning presented transaction.
306
The arbiter sends control signals to control flip flops to configure segmentation of the pathways in an interconnect to establish a connection between the winning presenting-initiator network-resource and the available-target network resource.
308
The initiating network resource transitions the voltage levels on the multi-bit word lines across the shared interconnect to communicate the payload of information to the target network resource.
310
End

Figure 3

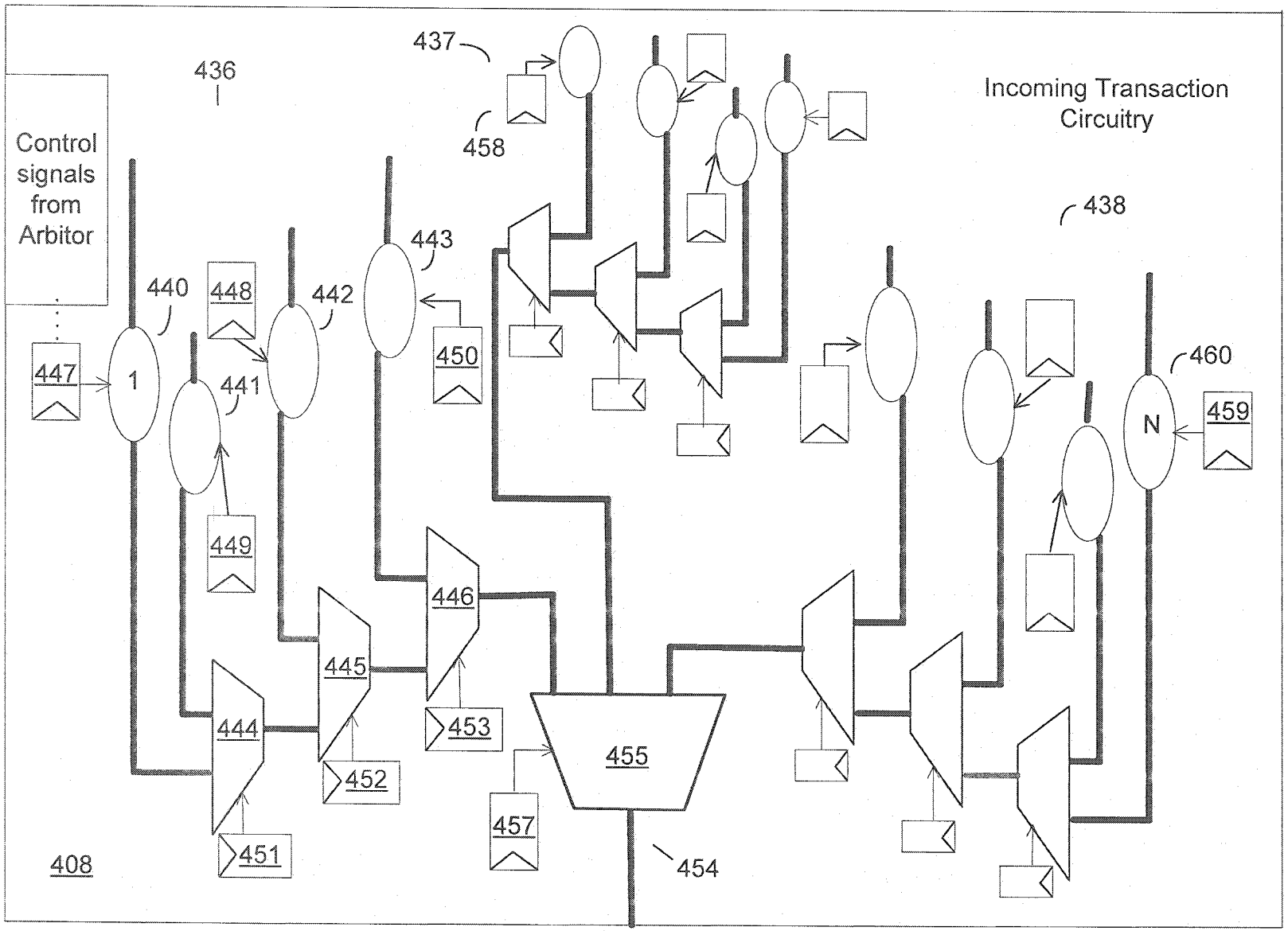
436
437
458
Incoming Transaction Circuitry
438
Control signals from Arbitor
440
448
442
443
447
1
441
450
460
459
N
449
446
445
444
453
455
452
457
451
454
408

Figure 4

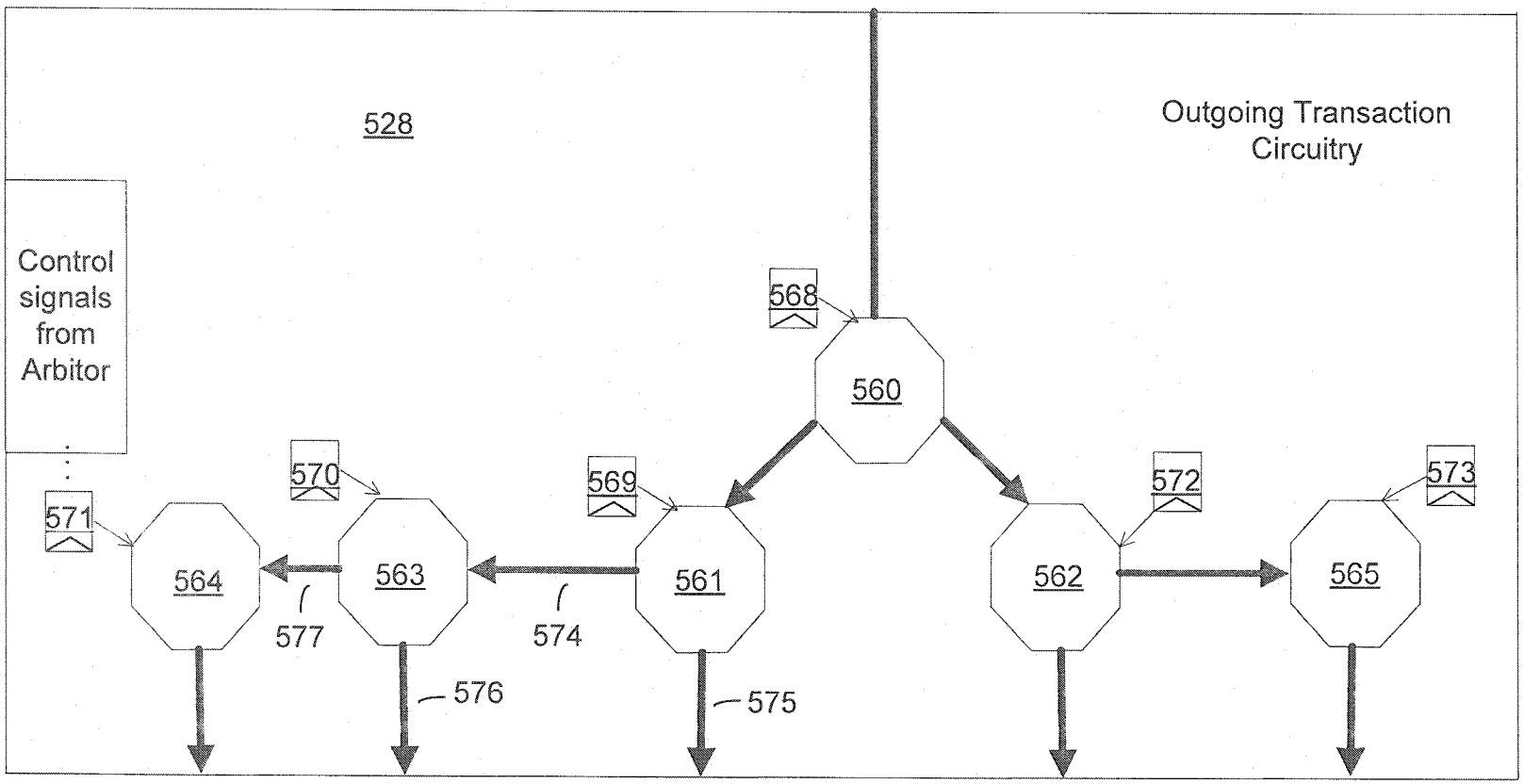
528
Outgoing Transaction Circuitry
Control signals from Arbitor
568
560
570
569
572
573
571
564
563
561
562
565
577
574
576
575

Figure 5